

In the Claims:

1. (currently amended) A switch for regulating the substrate potential of an integrated circuit comprising:

a first control input for controlling said switch coupled to a first N-well bias supply line;

a second control input for controlling said switch coupled to a substrate bias supply line;

a first switched terminal of said switch coupled to a ground;

a second switched terminal of said switch coupled to said substrate bias supply line; and

an output terminal of said switch coupled to a P-type substrate.

2. (original) The switch of Claim 1, wherein said switch is operable to electrically couple said P-type substrate to said ground when a bias voltage is present on said first N-well bias supply line.

3. (original) The switch of Claim 1, wherein said switch is operable to electrically couple said P-type substrate to said substrate bias supply line when a substrate bias voltage is present on said substrate bias supply line.

4. (currently amended) The switch of Claim 1, further comprising a second third control input for controlling said switch coupled to a second N-well bias supply line.

5. (original) The switch of Claim 4, wherein said switch is operable to electrically couple said P-type substrate to said ground when a bias voltage is present on said second N-well bias supply line.

6. (original) The switch of Claim 4, wherein said switch is operable to electrically couple said P-type substrate to said substrate bias supply line when a substrate bias voltage is present on said substrate bias supply line.

7. (original) The switch of Claim 1, wherein said switch is operable to electrically couple said P-type substrate to said substrate bias supply line when a substrate bias voltage is present on said substrate bias supply line and there is no bias voltage present on said N-well bias line.

8. (original) The switch of Claim 1, wherein said switch is operable to electrically couple said P-type substrate to said ground when a substrate bias voltage is present on said substrate bias supply line and there is no bias voltage present on said N-well bias line.

Claims 9-20 (canceled) (various restrictions)

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